

1/5

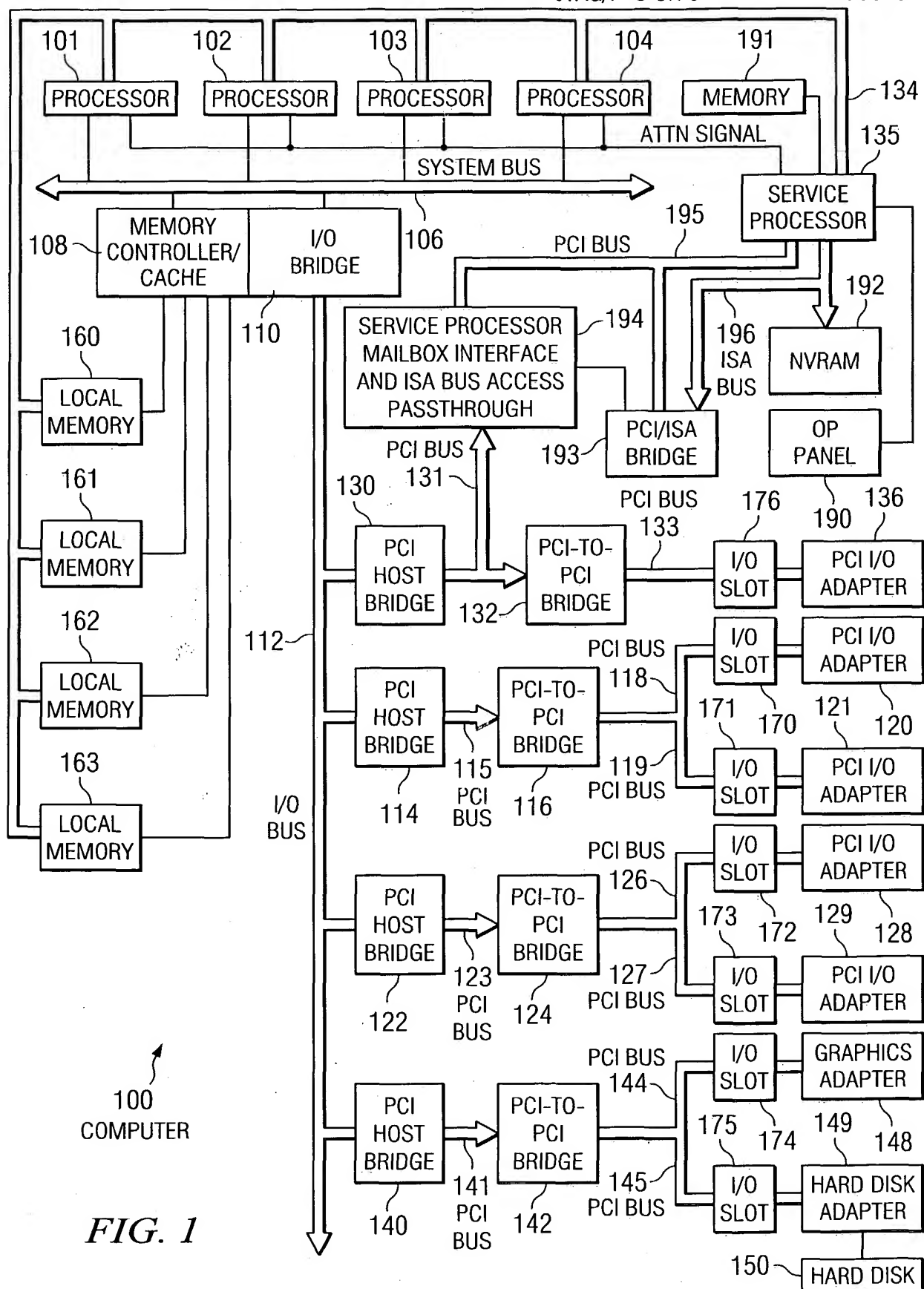
JTAG/I²C OR OTHER SERIAL BUSSES

FIG. 1

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FIG. 2

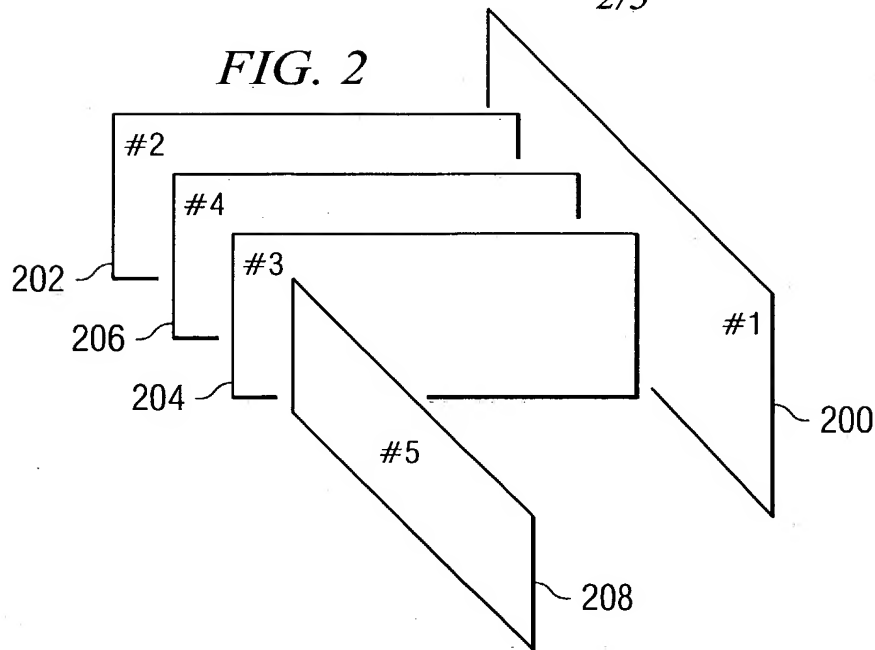
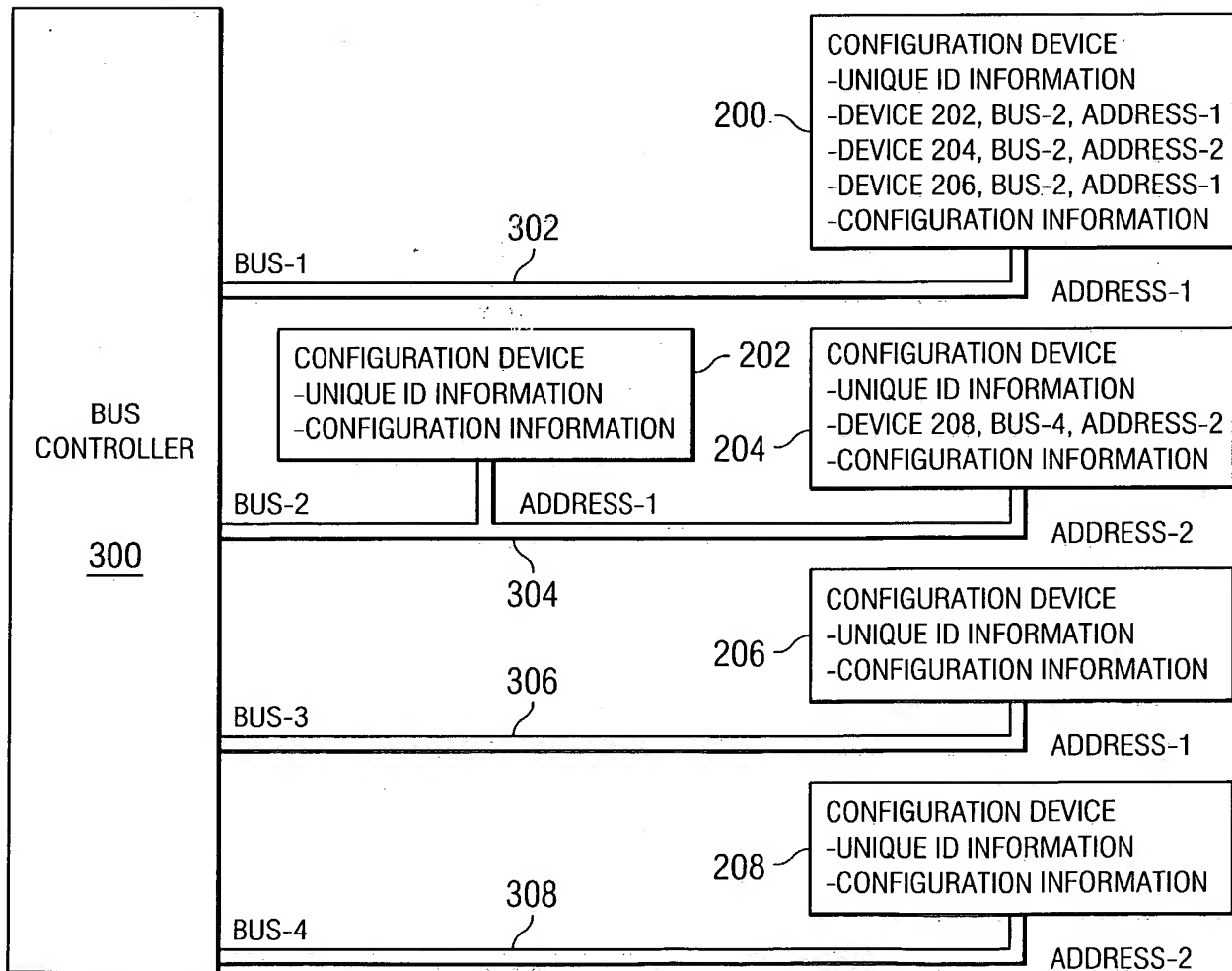


FIG. 3



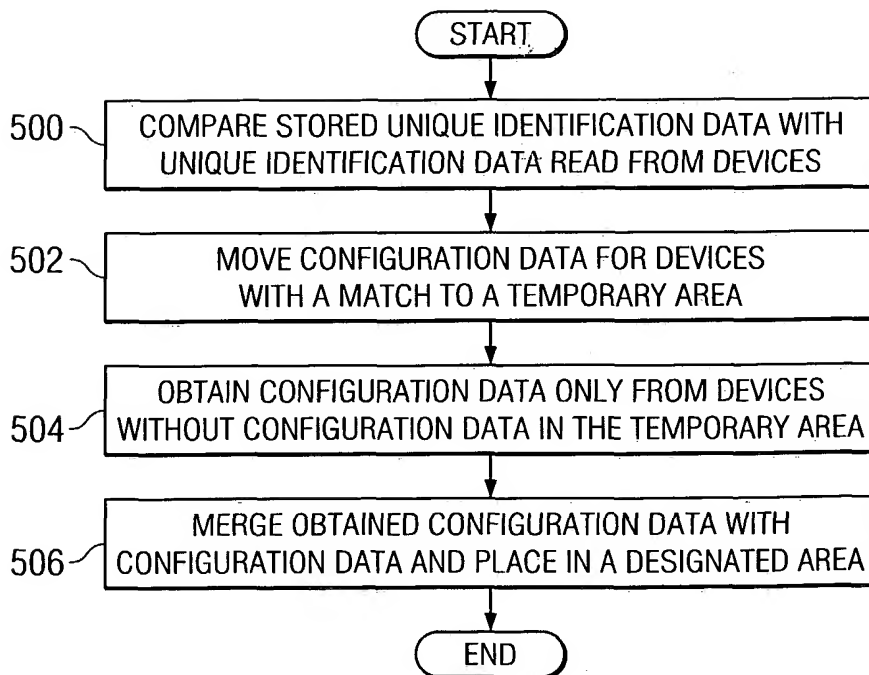
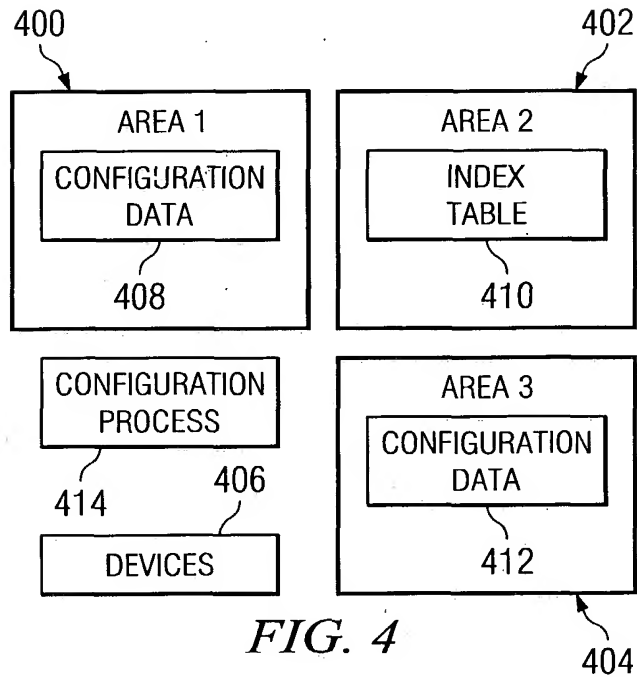


FIG. 5

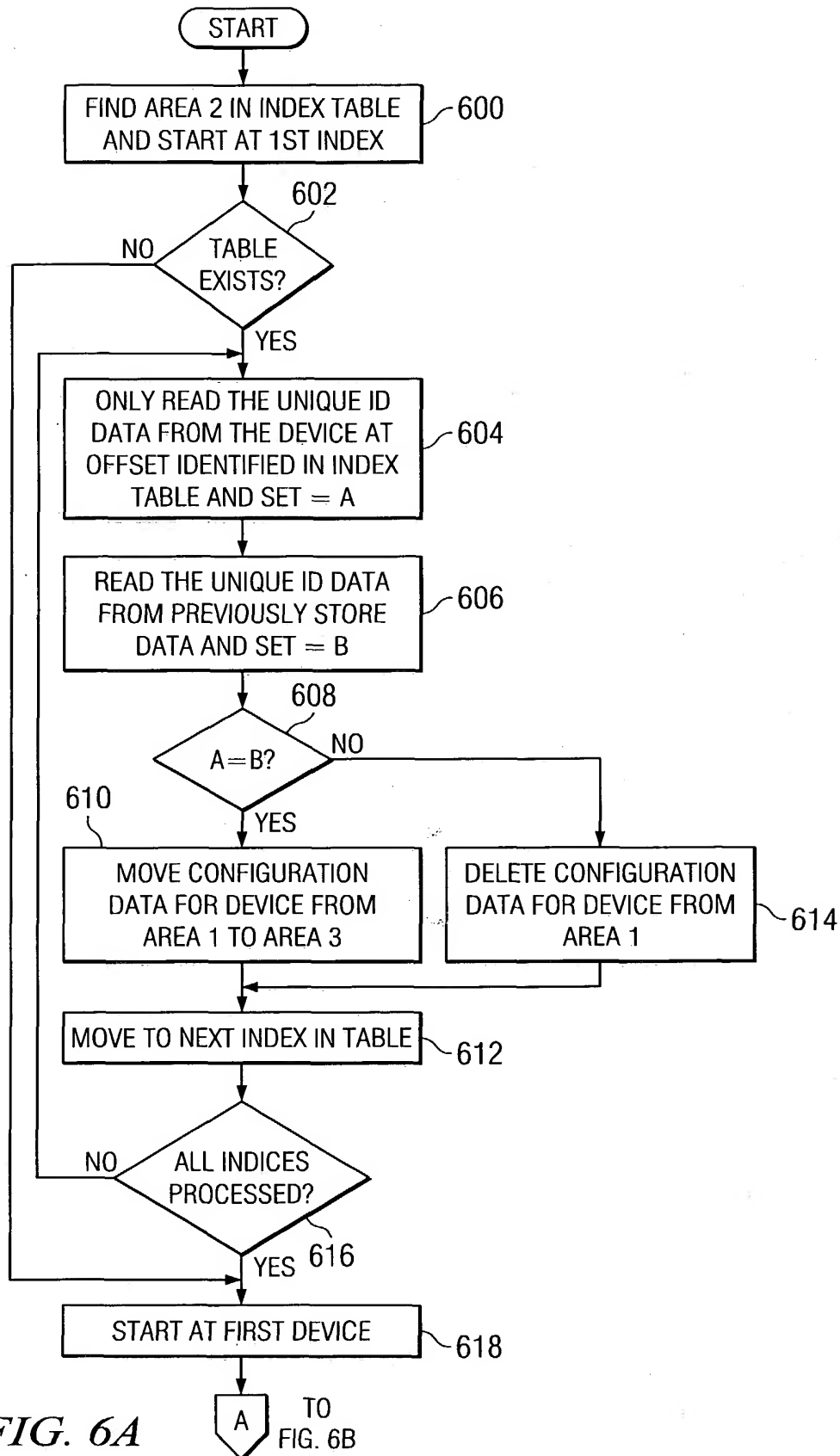


FIG. 6A

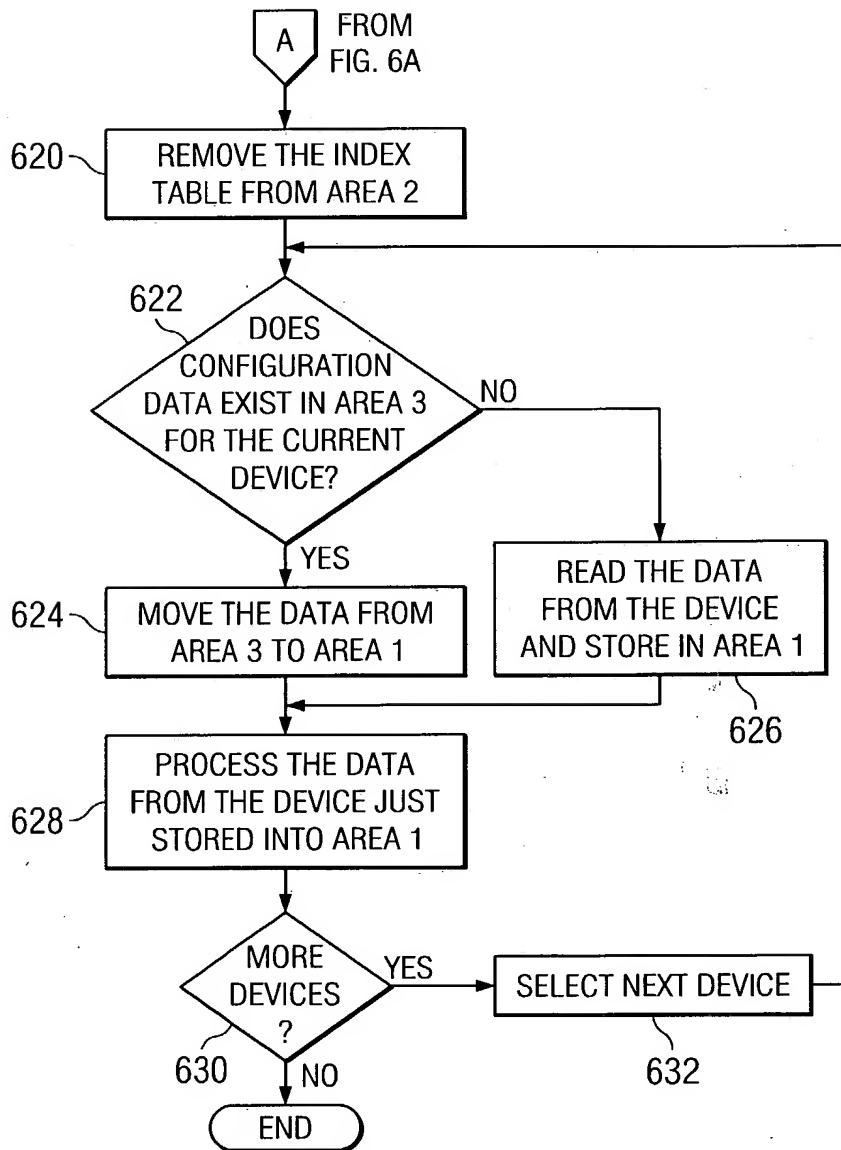


FIG. 6B